

A

A

B

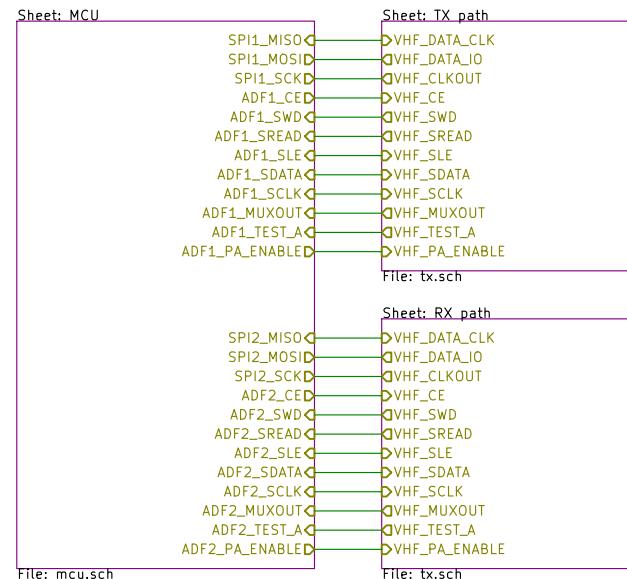
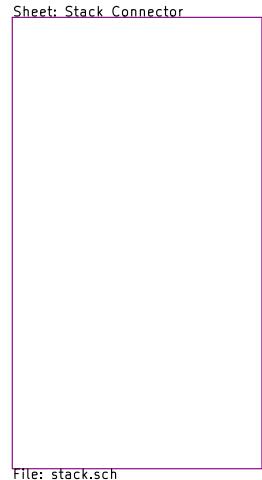
B

C

C

D

D



VHF: 144 – 146 MHz
UHF: 430 – 440 MHz

SPI1 is used for VHF Downlink
SPI2 is used for UHF Uplink
SPI3 is used for storage
UART4 is debug serial

SPI Mode

In SPI mode, the TxRxCLK pin is configured to input transmit data in transmit mode. In receive mode, the receive data is available on the TxRxDATA pin. The data clock in both transmit and receive modes is available on the CLKOUT pin. In transmit mode, data is clocked into the ADF7021 on the positive edge of CLKOUT. In receive mode, the TxRxDATA data pin is sampled by the microcontroller on the positive edge of the CLKOUT. To enable SPI interface mode, set R0_DB28 high and set R15_DB[17:19] to 0x7. Figure 8 and Figure 9 show the relevant timing diagrams for SPI mode, while Figure 60 shows the recommended interface to a microcontroller using the SPI mode of the ADF7021.

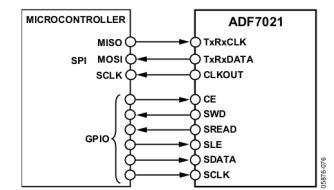


Figure 60. ADF7021 (SPI Mode) to Microcontroller Interface

UHF RX Uplink

A VHF and UHF packet radio for the sky

Nick Østergaard

AAUSAT6

Sheet: /

File: vhf_uhf_com.sch

Title: SKYPEATER

Size: A4 Date:

KiCad E.D.A. kicad (5.1.4-26-gdcd31f570)

Rev: 1.0

Id: 1/5

- Improvement suggestions:
- Add "sniffer" port aka power splitter after LNA to another SMA to connect to external SDR
 - Add more GPIO to the external switching connector
 - Separate LDO for ADF TCXO
 - Make 10µF caps with a bigger footprint for example Capacitor_SMD:C_1812_4532Metric for the PA
 - Shall we add a Harwin M83 interface too?
 - CAN
 - Power
 - Ethernet?

A

B

C

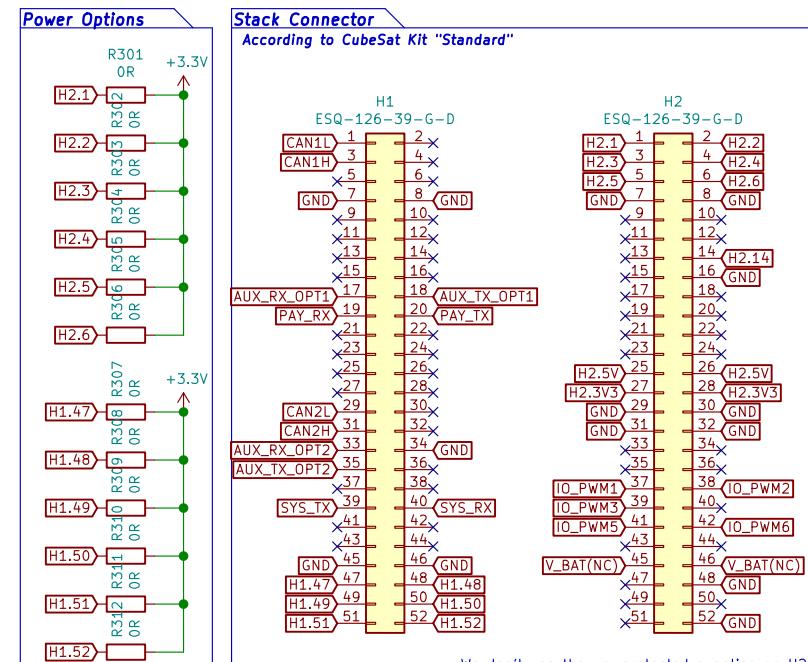
D

A

B

C

D



NOTE: <something>(NC) are not connected stack pins, the netlabels are used to make the mirror header connect to it as a mirror.

A VHF and UHF packet radio for the sky

Nick Østergaard

AAUSAT6

Sheet: /Stack Connector/

File: stack.sch

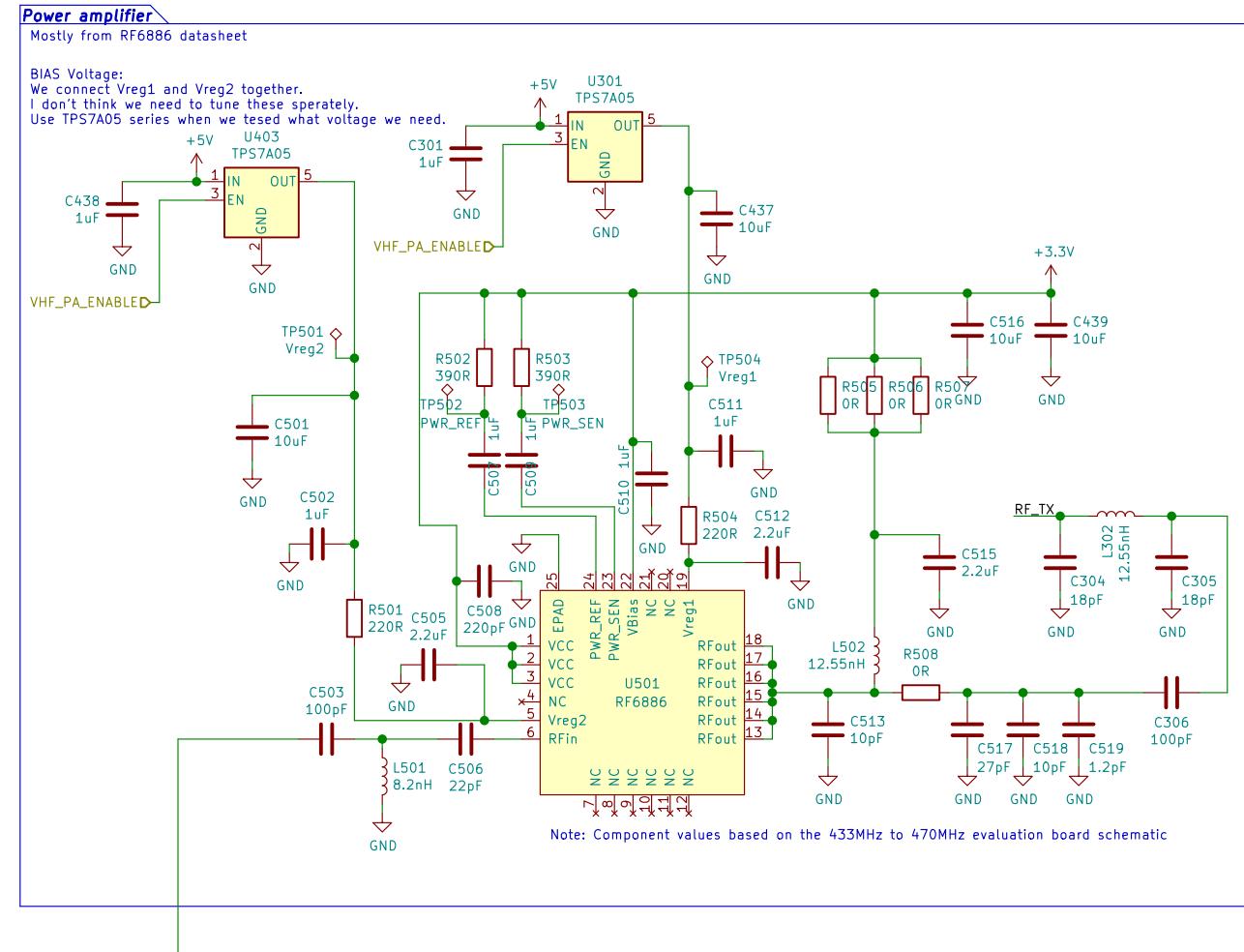
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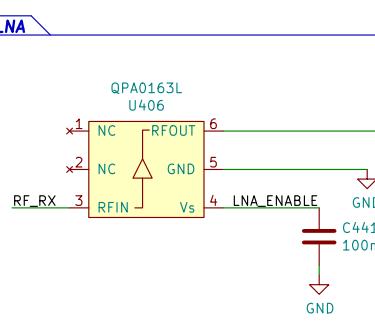
KiCad E.D.A. kicad (5.1.4-26-gdcd31f570)

Rev: 1.0

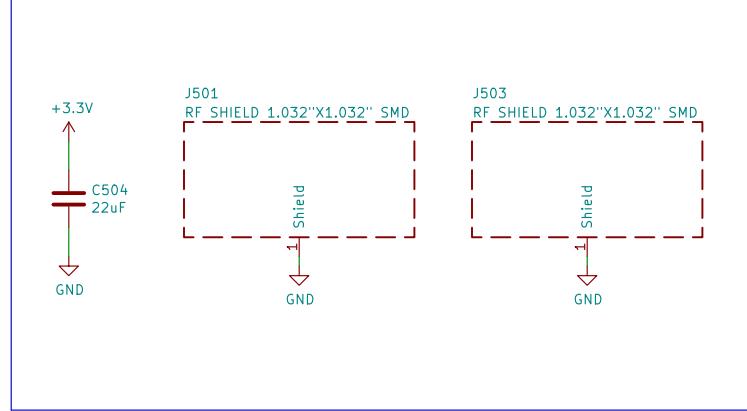
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Note: Component values based on the 433MHz to 470MHz evaluation board schematic

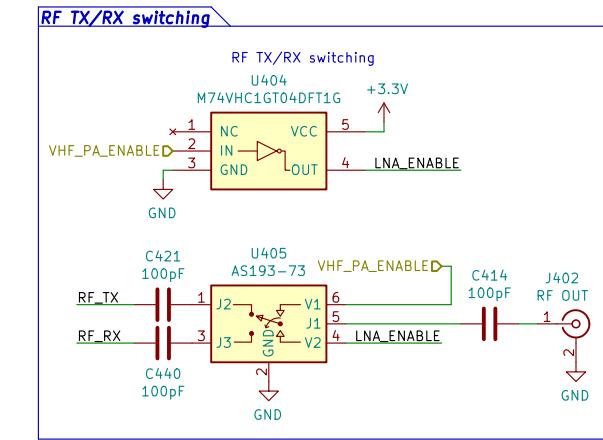


Miscellaneous things



Potential PA 5V:
TQP7M9105
TQP7M9106
MMZ09332BT1
RFP3800
BGA2776
RF6886
GRF5115
SKY65366-21 for
SKY65367-11 fo

Potential PA 7
AFIC901NT1
RFPA3800
TAT7427B



The IF Filter response can be viewed on a spectrum analyzer by AC coupling

10

In SPI mode, the TxRxCLK pin is configured to input transmit data in transmit mode. In receive mode, the receive data is available on the TxRxDATA pin. The data clock in both transmit and receive modes is available on the CLKOUT pin. In transmit mode, data is clocked into the **ADF7021** on the positive edge of CLKOUT. In receive mode, the TxRxDATA data pin is sampled by the

microcontroller on the positive edge of the CLKOUT. To enable SPI interface mode, set R0_DB28 high and set R15_DB[17:19] to 0x7. Figure 8 and Figure 9 show the relevant timing diagrams for SPI mode, while Figure 60 shows the recommended interface to a microcontroller using the SPI module of the ADT9231.

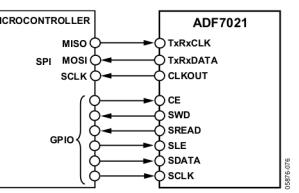
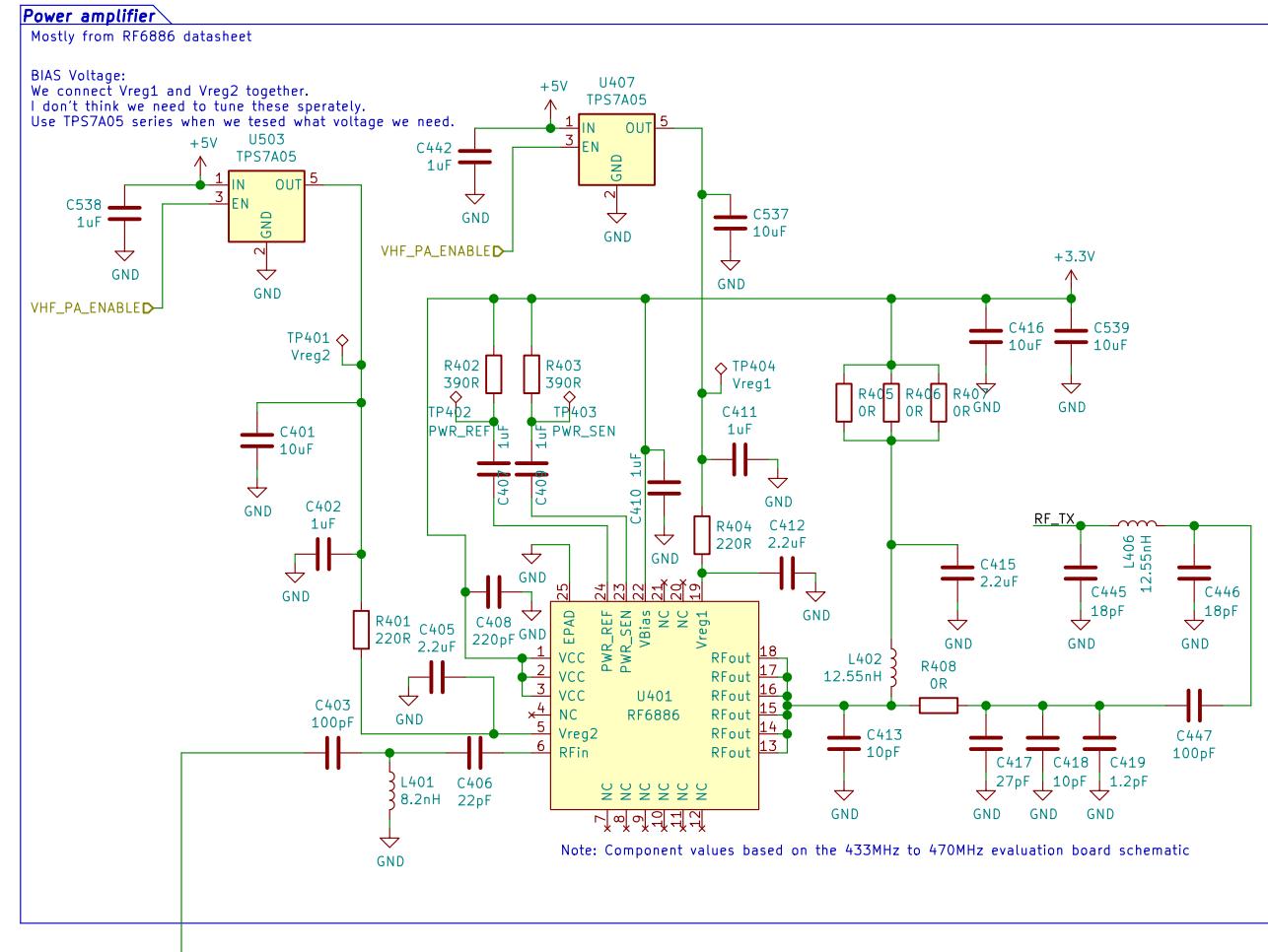
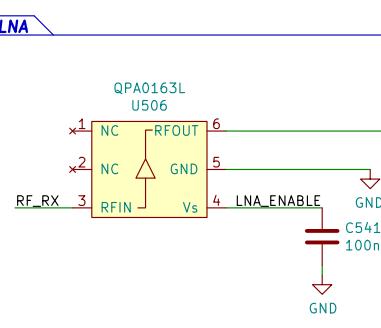


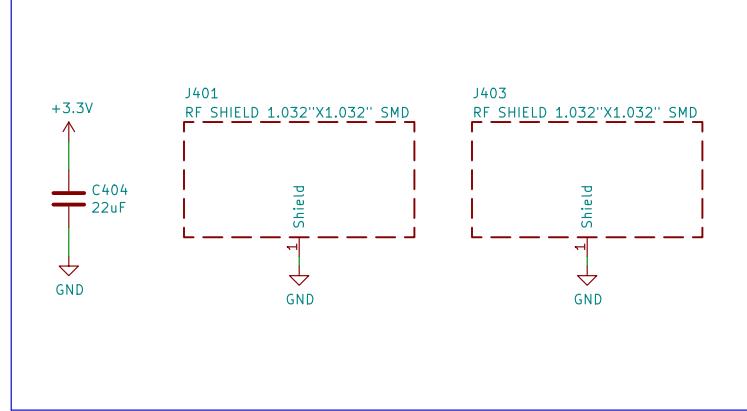
Figure 60. ADF7021 (SPI Mode) to Microcontroller Interface



Note: Component values based on the 433MHz to 470MHz evaluation board schematic

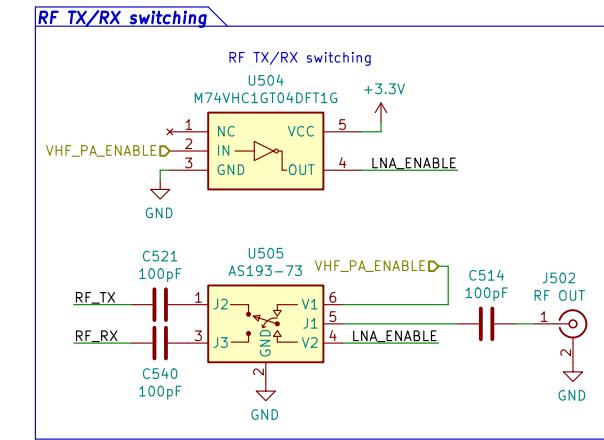


Miscellaneous things



Potential PA 5V:
TQP7M9105
TQP7M9106
MMZ09332BT1
RFPA3800
BGA2776
RF6886
GRF5115
SKY65366-21 for
SKY65367-11 fo

Potential PA 7
AFIC901NT1
RFPA3800
TAT7427B



The IF Filter response can be viewed on a spectrum analyzer by AC coupling

ck
ply.

e

In mode, the TxRxCLK pin is configured to input transmit mode. In receive mode, the receive data is available RxDATA pin. The data clock in both transmit and receive available on the CLKOUT pin. In transmit mode, data into the **ADF7021** on the positive edge of CLKOUT. mode, the TxRxDATA data pin is sampled by the **ADF7021** on the positive edge of CLKOUT.

SPI interface mode, set R0_DB28 high and set R17:19] to 0x7. Figure 8 and Figure 9 show the relevant diagrams for SPI mode, while Figure 60 shows the extended interface to a microcontroller using the SPI ADIET231.

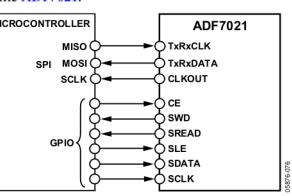


Figure 60. ADF7021 (SPI Mode) to Microcontroller Interface

